DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

SEMINAR ON DESIGN OF ELECTRICAL AND ELECTRONICS CIRCUITS USING E-CADD AND ORCAD SOFTWARE

REPORT

Event Title	**DESIGN OF ELECTRICAL AND ELECTRONICS CIRCUITS USING E-CADD AND ORCAD SOFTWARE**		
Resource Person	**Mrs.JAYALAKSHMI**		
Academic year	2023-2024		
Organizing by	Department of Electrical and Electronics Engineering, Jaya Engineering College.		
Program Type (Workshop / Motivation speech / Field Visit / Competition / Others)	Seminar		
Start date & End Date (DD/MM/YYYY)	19-08-2023		-
Duration of the activity (in Mins) & Start Time & End Time	Duration: 120 Mins	Start Time: 11.00 AM	End Time: 1.00 PM
Participants	Students: 45	Faculty: 6	External:
Mode of session (online / offline)	Offline		
Event coordinator	**Dr. G.GOMATHY, HOD/EEE** Jaya Engineering College.		
Target Participants	III & IV Year students from Department **of EEE, E&I, MECH & Aero** Jaya Engineering College.		
Outcome	The students had a deep understanding about **E-CADD AND ORCAD SOFTWARE** using electrical and electronic circuit		

Department of Electrical and Electronics Engineering organized a Seminar of design of electrical and electronics circuits using e-cadd and orcad software ,on 19th AUG 2023 through offline mode, venue at EEE Seminar hall, EEE Block in our institution. The Event started at 11.00 A.M with a warm welcome address by the Dr.G.Gomathy Professor, Department of Electrical and Electronics Engineering, JEC.

The session was started by the resource person Mrs.Jayalakshmi cadd training centre as Seminar on "design of electrical and electronics circuits using e-cadd and orcad software. The resource person has kindled the curiosity of the entire student participants on their innovative thinking and idea formulation.

The session mainly focused on In the beginning the Resource Person gave the introduction on e-cadd and orcad software, OrCAD is a software product family for professional PCB design, printed circuit board (PCB) layout, simulation, and routing. OrCAD PCB Editor is powered by the Cadence Allegro engine and offers you fast and predictable results. OrCAD can be used in a constraint driven design methodology, where design rules are managed in the Constraint Manager. These rules which are captured while entering the schematic and are checked in real time as design rule checks (DRC)

The vote of thanks was given by S.Vijayasarathi Assistant professor, Department of Electrical and Electronics Engineering, Jaya Engineering College. He gave best wishes to all students and faculty members for delightful and successful day. As per the feedback received from the participants it was very good learning experience, the Seminar helped them in understanding the recent technology used in Designing of electric circuit used in e-cad

Event coordinator   HOD   PRINCIPAL

A report Photos Seminar on the Topic
"DESIGN OF ELECTRICAL AND ELECTRONICS CIRCUITS USING E-CADD AND ORCAD SOFTWARE" at Jaya Engineering College, Thiruninravur, on 19th April 2023.