

## ORGANIZING COMMITTEE

### CHIEF PATRON:

**Prof .A. Kanagaraj**  
Chairman,  
Jaya Educational Trust

**Mrs.K.VijayaKumari**  
Secretary,  
Jaya Educational Trust

### PATRON:

**Dr.Krishnan Vijayaragavan**  
Principal, JEC.  
**Dr. E. Kaliappan**  
Vice Principal, JEC

### CONVENOR

**Prof. R. Narayanan**  
HoD/ ECE

### CO-ORDINATOR:

**Dr. M. Gopu**  
Professor / ECE  
Jaya Engineering College  
Chennai 602024  
Mobile: 9840540933

Venue: MB 108, Main Block, JEC

### Important Dates:

Submission of Application : 10/09/16  
Intimation of Selection : 15/07/16  
Conformation by  
Participants : 18/07/16

## OBJECTIVES

To stimulate the students to develop simple VHDL applications and to explore the various oppertunities in the VHDL Companies.

## TOPICS TO BE COVERED

- Introduction about VHDL.
- Hardware Modelling
- Sequential, concurrent statements and FSM synthesis in VHDL
- Functions, Packages declaration in VHDL and Implementation of Digital Circuits in Spartan 6 FPGA Board.

## ADDRESS FOR CORRESPONDANCE

**Dr. M. Gopu**  
Professor / ECE  
Jaya Engineering College  
Chennai – 602024  
Mobile: 9840540933  
Office : 044 - 26390041  
Email : [gopumano@gmail.com](mailto:gopumano@gmail.com)

## ONEDAY WORKSHOP

ON

## VLSI PROGRAMMING

(23.07.2016)

Co-ordinator

**Dr. M. Gopu**  
Professor / ECE

Organized by



Department of Electronics  
and Communication  
Engineering  
Jaya Engineering College  
Thiruninravur -602024  
Chennai

## ABOUT THE COLLEGE

Jaya Engineering College a premier technical institution approved by AICTE and affiliated to Anna University was established in 1995 with a focus on technological innovation, entrepreneurship and character building in the students. The institution has been established by Jaya Educational Trust, imparting 12 excellent educations through 21 educational institutions offering courses in medicine, paramedical and arts & science besides running many schools, catering the needs of the society. The college management team consists of well experienced and committed Academicians and Administrators.

## ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering (ECE) was established in the year 1995 with the vision of developing the department in the field of Electronics and Communication Engineering and make the students blossom into meritorious, self-disciplined and hard worker. It offers undergraduate course, Post Graduate course and various Research programmes.

## ABOUT THE WORKSHOP

The main purpose of this workshop is to provide an opportunity for engineering faculty from Academic Institutions, engineers from Industries, under and Post Graduate Students to get the exposure and interaction with the field experts, who are involved in the research and development activities in VLSI Design. Eminent professors will be invited to deliver special lectures and demonstrations.

## RESOURCE PERSONS

Session will be handled by Dr. M. Gopu, Professor, Mr. Rajamoorthy, Assistant Professor, Mrs Tamarasi, Assistant Professor, Department of ECE, Jaya Engineering College, Chennai.

## REGISTRATION

- Participants will be selected on first come first serve basis
- Registration fees for participants is Rs. 250
- Selected candidates will be intimated through E- mail.
- The Registration Fees includes Tea/Coffee, Lunch, Workshop Kit and Participation Certificate.

## REGISTRATION FORM ONE DAY WORKSHOP ON VLSI PROGRAMMING

### PERSONAL DETAILS

Name :  
Department :  
Qualification :  
Gender :  
Name of the Institution :  
Address for Communication :  
Phone :  
Email :  
Accommodation required : Y/N

Signature

Mr/Ms ----- is recommended to attend the ONE DAY WORKSHOP ON VLSI PROGRAMMING to be conducted in your college.

Signature of Sponsoring Authority  
With date and seal