

JAYA GROUP OF INSTITUTIONS- THIRUNINRAVUR

4TH SEMESTER – B.Tech

INTERNAL ASSESSMENT –III (MODEL EXAMINATION III)

Sub Name: Microprocessor and Microcontroller

Date: 9/04/2015

Sub Code: EC6504

Branch: IT

Duration: 3Hours

Max Marks: 100

PART A(10X2=20) Answer All Questions

1. Define Architecture
2. What is the difference between procedure and macro?
3. What is meant by closely coupled configuration?
4. What are the different bus allocation schemes?
5. What is meant by Interfacing?
6. What is the difference between memory mapped I/O and I/O mapped I/O?
7. Write few advantages of serial communication
8. What is the difference between microprocessor and microcontroller?
9. Write few applications of stepper motor?
10. List out the interrupts of 8051.

PART B (5X16=80) Answer the Questions as per the Choice

11. a.i.Explain the architecture of 8086 with neat diagram (10)
ii. Explain the addressing modes of 8086 with examples ? (6)

OR

- b. List out the complete instructions set of 8086 (16)
12. a. Discuss in detail about multiprogramming. (16)

OR

- b. Explain coprocessor configuration and closely coupled configuration. (16)
13. a. Draw the block diagram of 8255 and explain its operating modes. (16)

OR

b. Draw the block diagram of 8279 and explain its operating modes (16)

14. a. Draw the architectural diagram of 8051 and explain its block (16)

OR

b. Explain the Interrupt structure of 8051 in detail. (16)

15. a. Explain the timer operation of 8051 with an example program. (16)

OR

b. Explain the interfacing of stepper motor and lcd display with 8051 (16)