

INTERNAL ASSESSMENT - I (MODEL EXAMINATION - I)

Sub.Name: MICROPROCESSOR AND MICROCONTROLLER

Date: 28/02/2015

Sub.Code: EC 6504

Branch: IT

Duration: 180 Minutes

Max.Marks: 100

PART – A (10X2=20) Answer all questions

1. Write the size of physical memory of 8086 microprocessor.
2. List out the advantages using segment register in 8086.
3. If execution unit generate effective address of 43A2H and the DS register contain 4000H, what will be the physical address generated by BIU? What is the maximum size of data segment?
4. What is the function of TF, DF and IF in 8086.
5. What is loader and linker.
6. Write the difference between maximum and minimum mode in 8086.
7. Explain the BHE and LOCK signal of 8086.
8. Write the advantages of multiprogramming.
9. Distinguish between HOLD and INTR.
10. What is meant polling?

PART – B (5X16=80) Answer the questions as per the choice

- 11(a). Explain in detail the architecture of 8086 microprocessor with neat diagram. (16)

Or

- (b). Write assembly language program for (i). finding largest number in an array. (8)

- (ii). addition of two 16 bit numbers. (8)

- 12(a). Explain the different types of instruction sets of 8086 with examples. (16)

Or

- (b). Explain interrupt and interrupt service routine in detail. (16)

- 13(a). (i) Explain in detail about procedure in 8086. (8)

- (ii) Explain assembler directives of 8086. (8)

Or

- (b). Explain the basic configurations of 8086 with required diagrams. (16)

14(a). Explain the signal description of 8086 with neat diagram.

(16)

Or

(b). Explain in detail about I/O programming.

(16)

15(a). Explain Loosely coupled configuration in detail with bus allocation schemes.

(16)

Or

(b). Write short notes on (i). Closely coupled configuration.

(5)

(ii). Coprocessor configuration.

(5)

(iii). System bus structure.

(6)